

08/256942	Class	Subclass
08/256942	ISSUE CLASSIFICATION	

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UTILITY SERIAL NUMBER 08/256942	PATENT DATE	PATENT NUMBER
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SERIAL NUMBER 08/256942	FILING DATE 05/31/94	CLASS 257	SUBCLASS 345	GROUP ART UNIT 2811	EXAMINER Loke
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APPLICANTS: MASAKAZU MORISHITA, HIRATSUKA-SHI, JAPAN.

CONTINUING DATA***
 VERIFIED THIS APPLN IS A CON OF 07/852,710 03/17/92 ABN
Loke

FOREIGN/PCT APPLICATIONS***
 VERIFIED JAPAN 3-52426 03/18/91
 JAPAN 3-52429 03/18/91
Loke

Foreign priority claimed 35 USC 119 conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	AS FILED	STATE OR COUNTRY JPX	SHEETS DRWGS. 16	TOTAL CLAIMS 35	INDEP. CLAIMS 3	FILING FEE RECEIVED \$1,040.00	ATTORNEY'S DOCKET NO. 35.C8301CI
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Verified and Acknowledged Loke Examiner's Initials

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#25
 TITLE: SEMICONDUCTOR DEVICE
 SEMICONDUCTOR DEVICE INCLUDING A GATE-INSULATED TRANSISTOR

U.S. DEPT. of COMM.-Pat. & TM Office-PTO-436L (rev. 10-78)

PARTS OF APPLICATION FILED SEPARATELY		Applications Examiner	
NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
Label Area		ISSUE BATCH NUMBER	
		PREPARED FOR ISSUE	
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